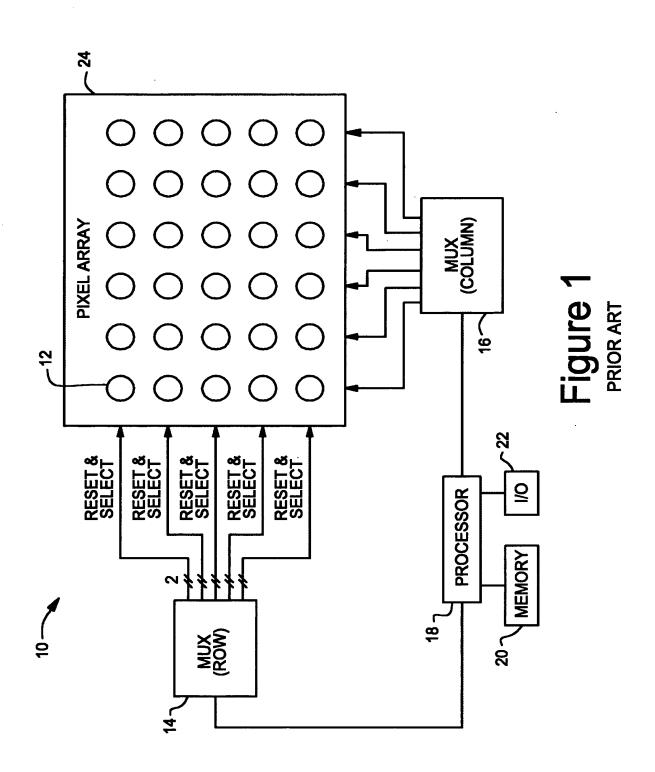


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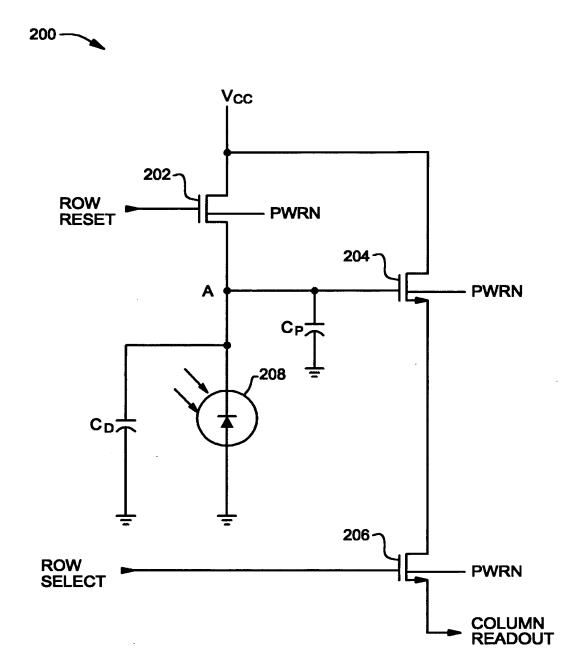


Figure 2 (PRIOR ART)



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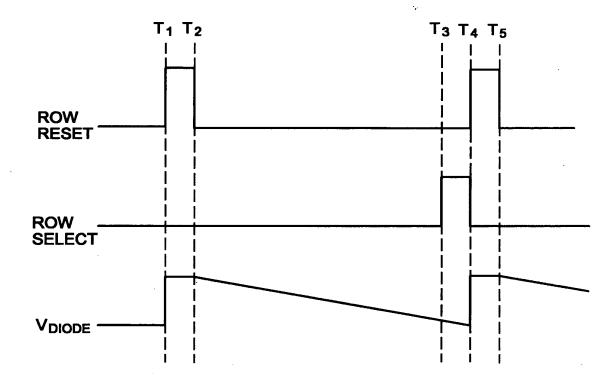
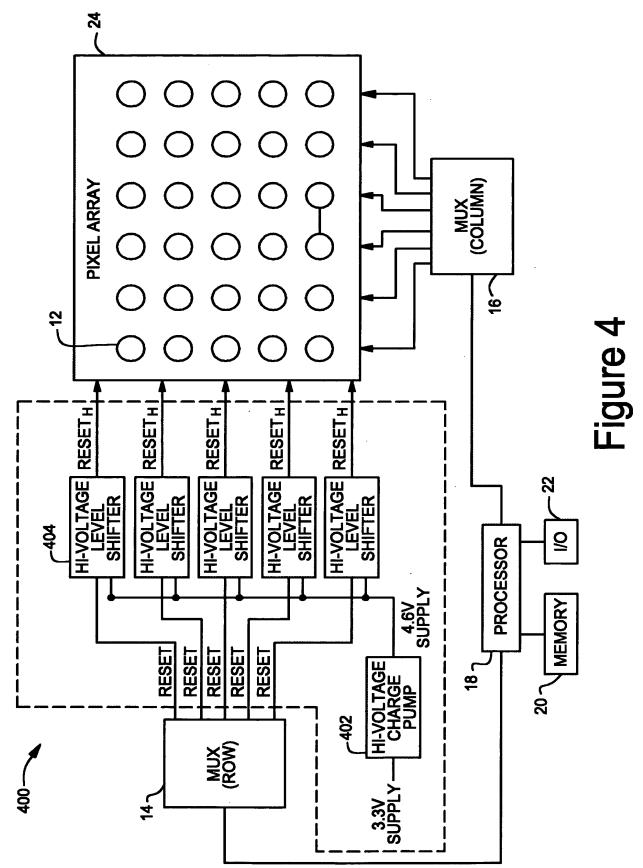


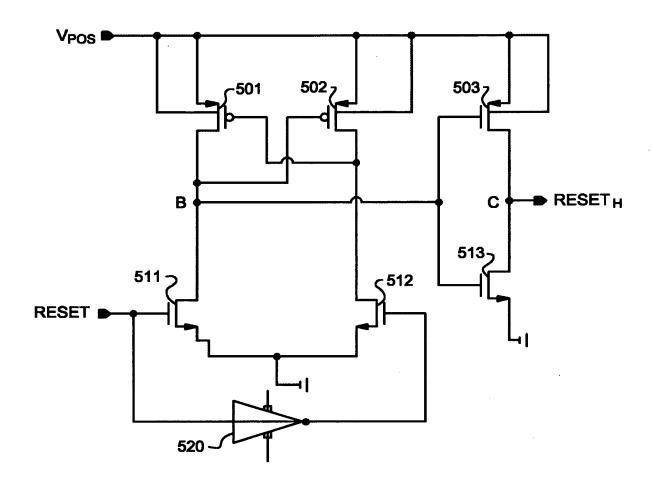
Figure 3

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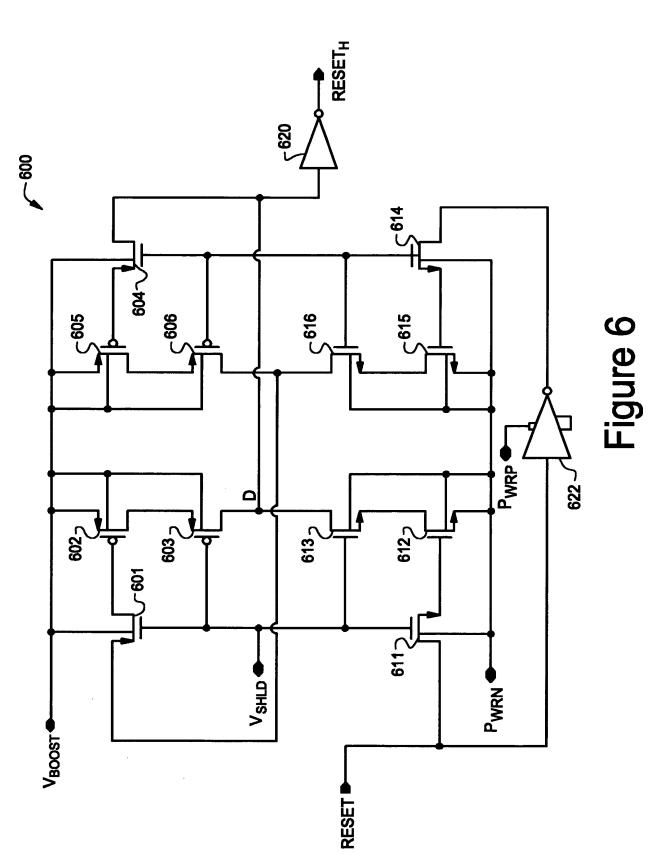


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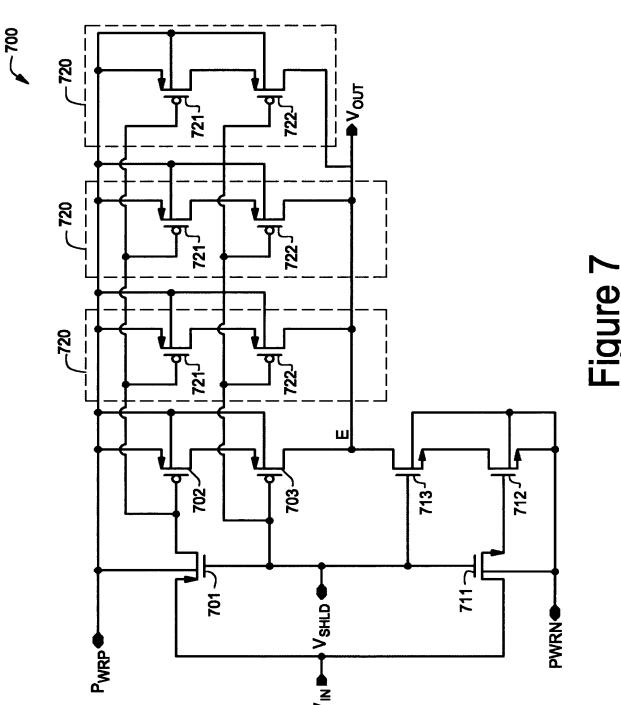


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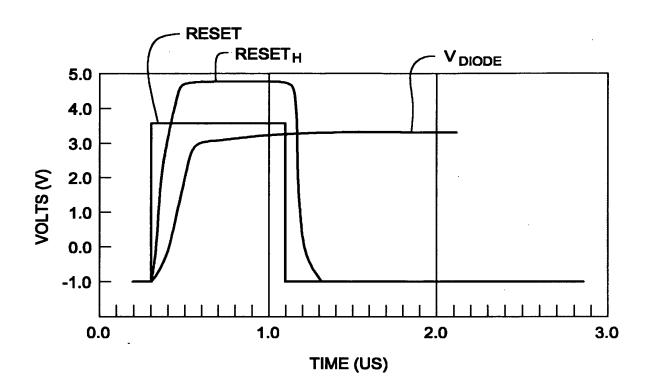




IIGH VOLTAGE RESET METHOD FOR IN ASING THE DYNAMIC RANGE OF A CMOS IMAGE ENSOR Inventors: Willem J. Kindt, Christina P. Phan, Shivani Gupta Docket No. M & G 50019.76US01/P05025

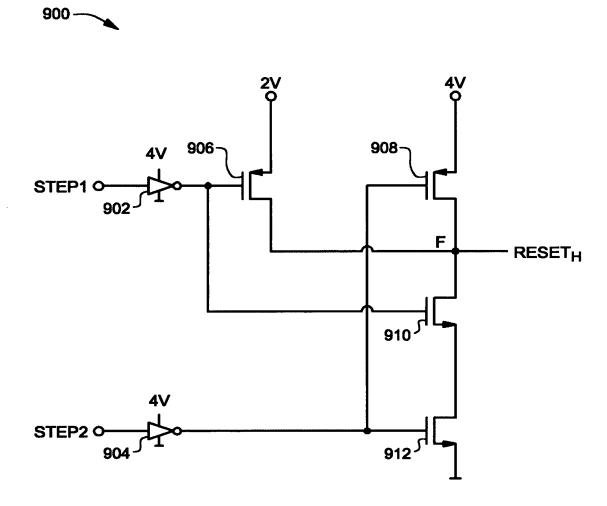






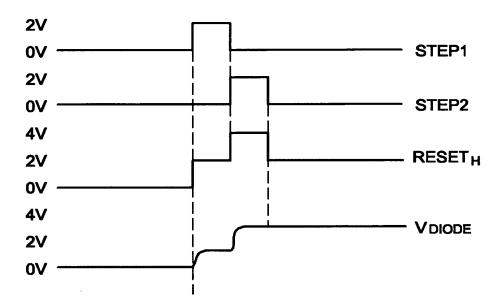


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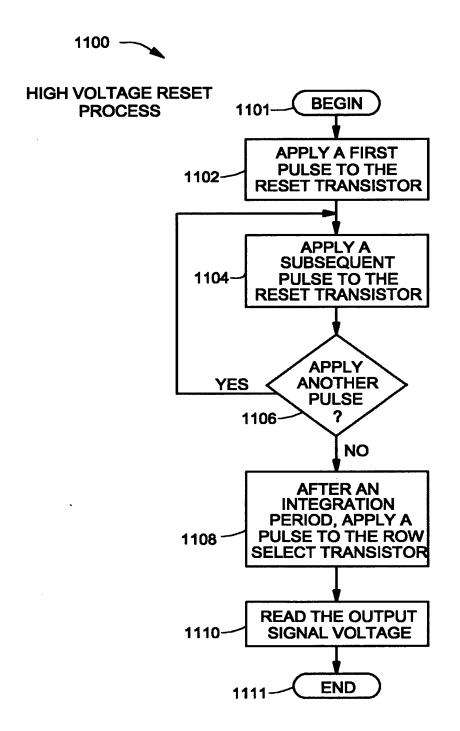


Figure 11